

**Amendments to the Specification:**

Please replace paragraph [26] with the following amended paragraph:

[26] As shown in Fig. 3, the task signal can be inputted into one of the task comparing units (e.g., 48c), and that task comparing unit can output a grant signal according to the address signal inputted based on the task signal. Here, a write signal (WR) that is generated from the central processing unit 10 can also be used. If the write signal (WR) is a prescribed value such as  $>1=$ , the write signal (WR) is a read signal (e.g., ~~read only~~), and if the write signal (WR) is set to  $>0=$ , the write signal (WR) is a write signal. However, it should be noted that the grant signal output is dependent or preferably totally dependent on the address signal. The address signal is preferably a signal for indicating whether a task is performed in the designated area or not.